



Commissioner for Patents
Washington, DC 20231
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Bib Data Sheet

CONFIRMATION NO. 2783

SERIAL NUMBER 09/964,484	FILING DATE 09/28/2001 RULE	CLASS 257	GROUP ART UNIT 2826	ATTORNEY DOCKET NO. H-1013	
APPLICANTS Toru Ishida, Tama, JAPAN; Tetsuharu Urawa, Kodaira, JAPAN; Fujio Ito, Hanno, JAPAN; Tomoo Matsuzawa, Tokyo, JAPAN; Kazunari Suzuki, Tokyo, JAPAN; Akihiko Kameoka, Ogose, JAPAN; Hiromichi Suzuki, Tokyo, JAPAN; Takuji Ide, Higashimurayama, JAPAN;					
** CONTINUING DATA ***** <i>not</i>					
** FOREIGN APPLICATIONS ***** JAPAN 2000-315785 10/16/2000					
IF REQUIRED, FOREIGN FILING LICENSE GRANTED ** 10/25/2001					
Foreign Priority claimed <input checked="" type="checkbox"/> yes <input type="checkbox"/> no 35 USC 119 (a-d) conditions <input checked="" type="checkbox"/> yes <input type="checkbox"/> no <input type="checkbox"/> Met after met <i>Allowance</i> Verified and Acknowledged <u>Examiner's Signature</u> <u>Initials</u>		STATE OR COUNTRY JAPAN	SHEETS DRAWING 21	TOTAL CLAIMS 31	INDEPENDENT CLAIMS 10
ADDRESS Mattingly, Stanger & Malur, P.C. Suite 370 1800 Diagonal Road Alexandria, VA 22314					
TITLE Semiconductor device having stacked semiconductor chips sealed with a resin seal member					
FILING FEE RECEIVED 1468	FEES: Authority has been given in Paper No. _____ to charge/credit DEPOSIT ACCOUNT No. _____ for following:		<input type="checkbox"/> All Fees <input type="checkbox"/> 1.16 Fees (Filing) <input type="checkbox"/> 1.17 Fees (Processing Ext. of time) <input type="checkbox"/> 1.18 Fees (Issue) <input type="checkbox"/> Other _____ <input type="checkbox"/> Credit		